

U.S. PATENT & TRADEMARK OFFICE
APR 28 2003

09683563

6591286

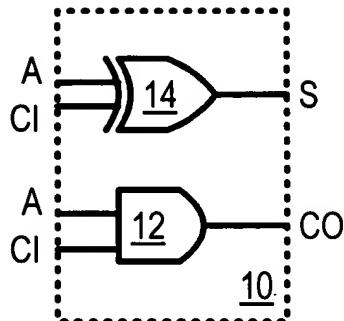


FIG. 1

PRIOR ART

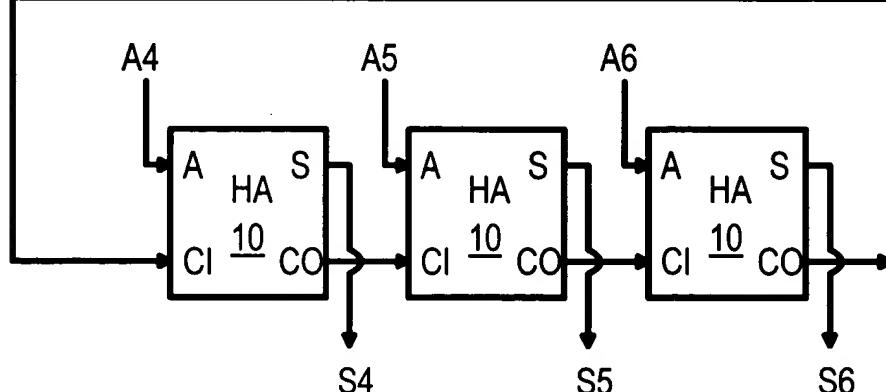
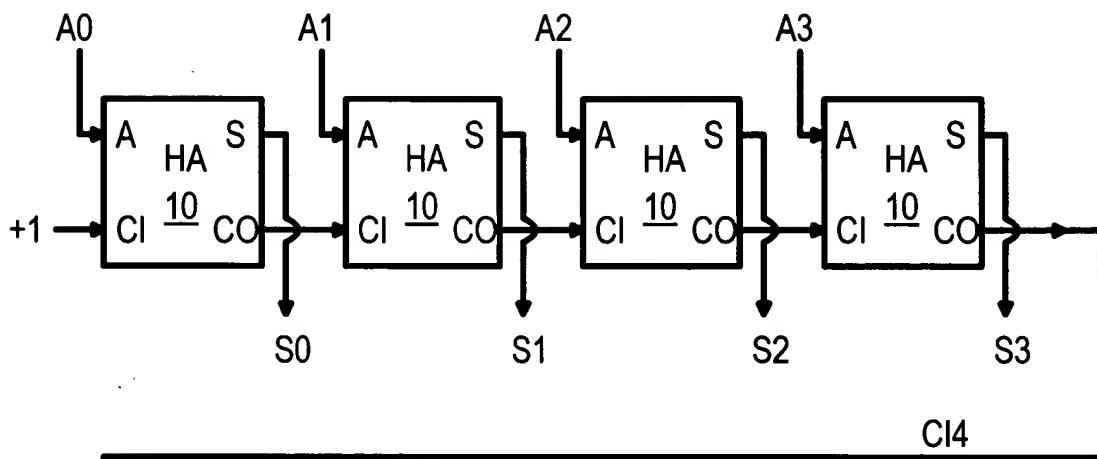


FIG. 2

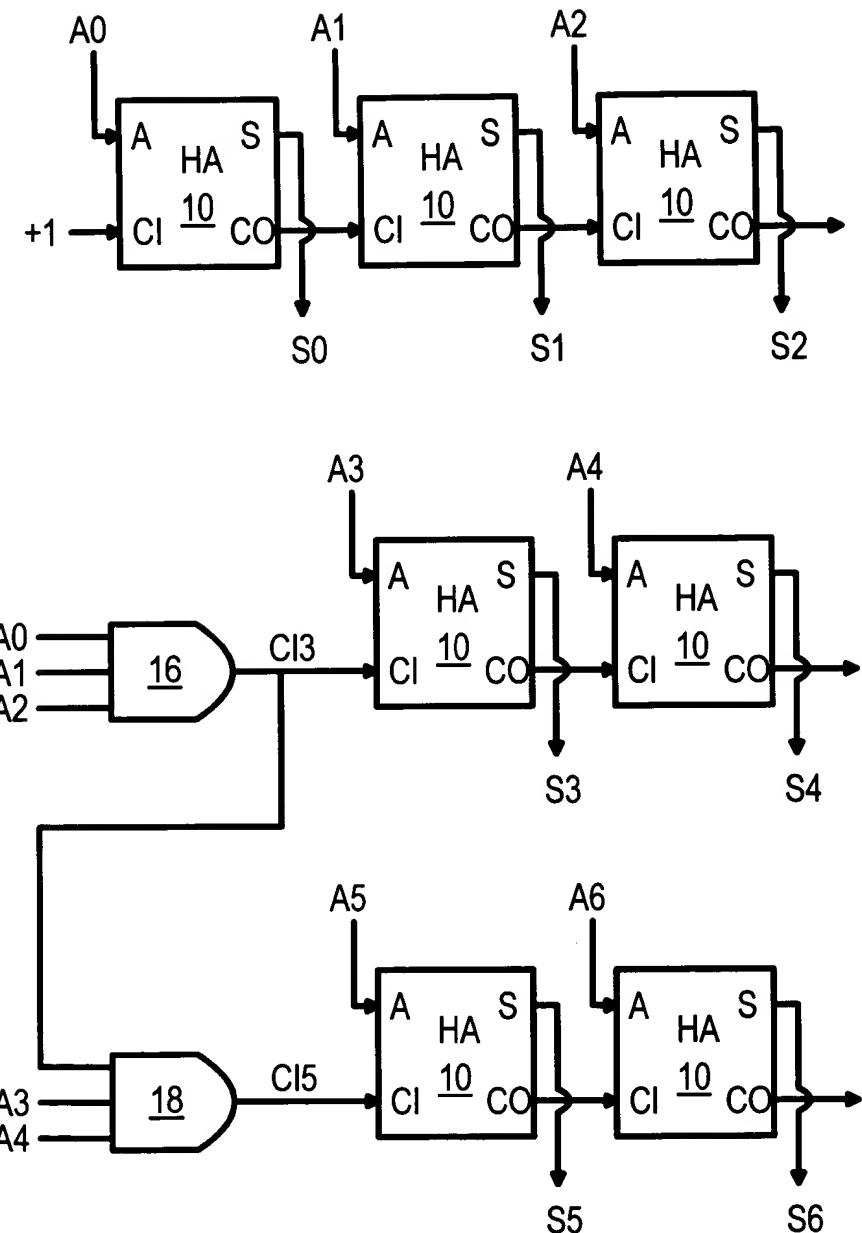
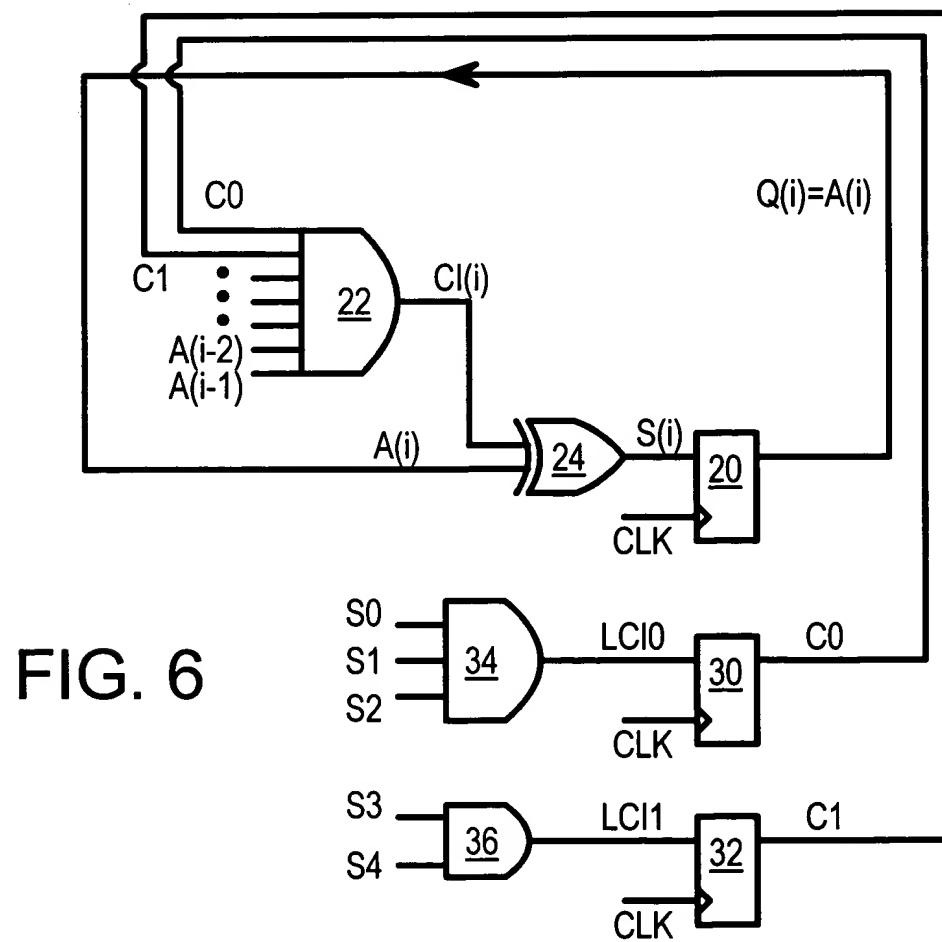
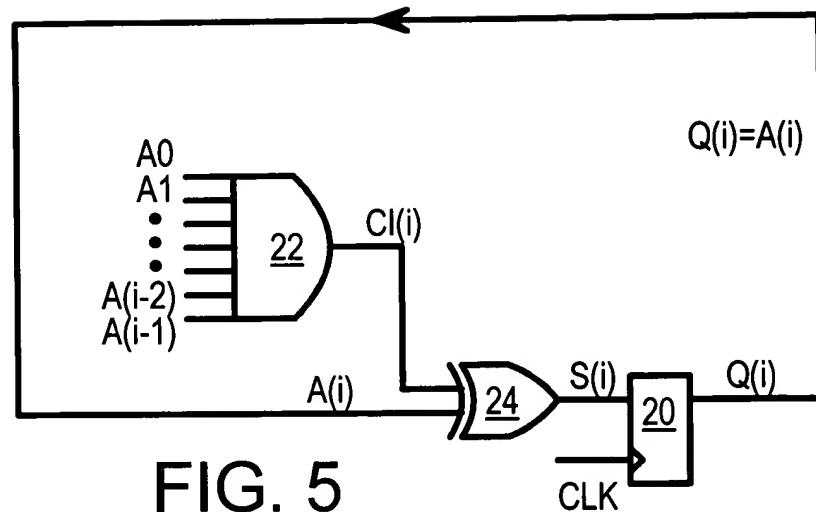


FIG. 3



<u>CURRENT STATE A</u>		<u>NEXT STATE S</u>
0000		0001
0001		0010
0010		0011
0011		0100
0100		0101
0101		0110
0110	LOW BITS = 111	0111
0111		1000
1000	XOR WITH HIGH BIT	1001
1001		1010
1010		1011
1011		1100
1100		1101
1101		1110
1110	LOW BITS = 111	1111
1111		0000
	XOR WITH HIGH BIT	

FIG. 4



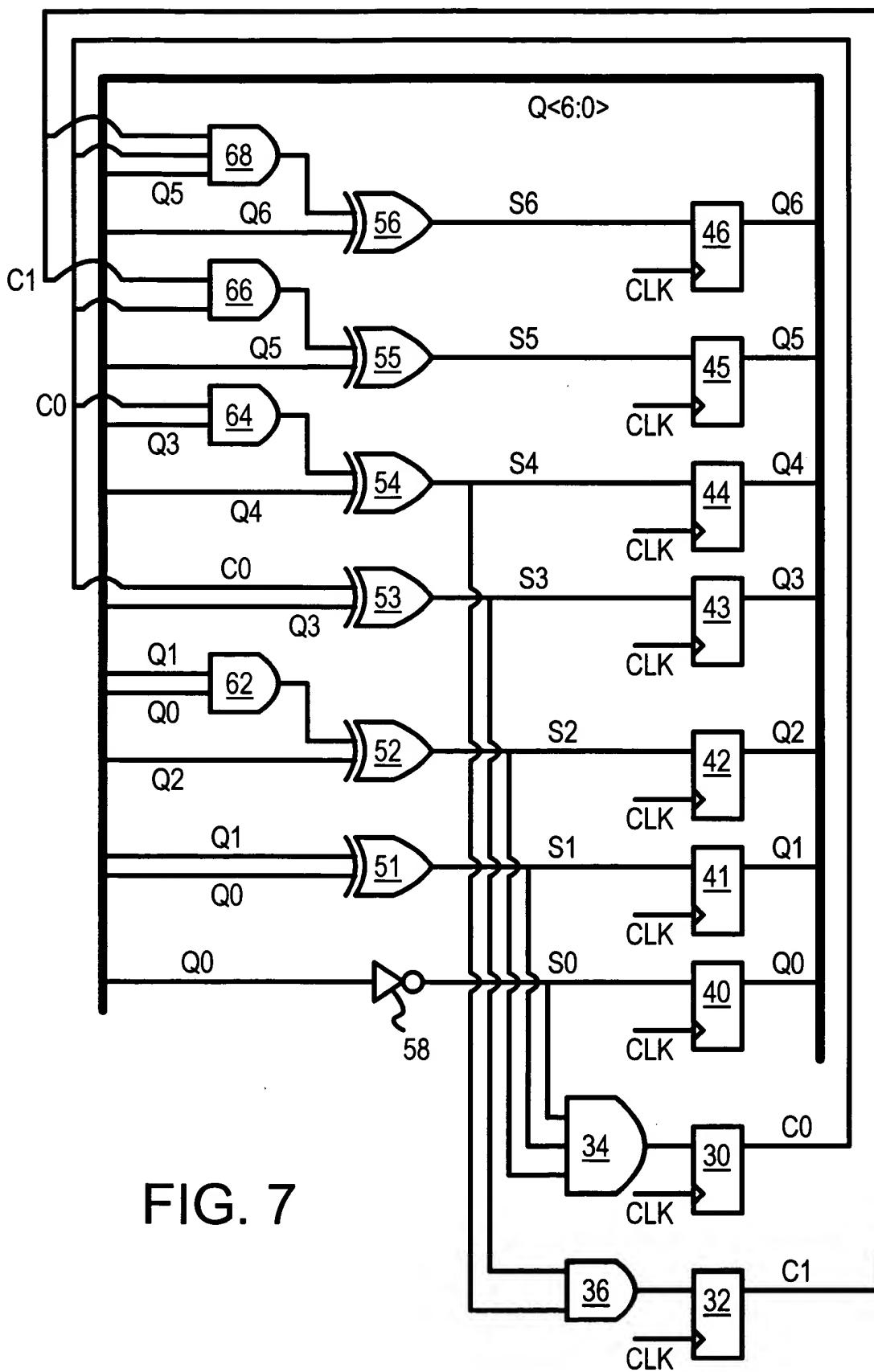


FIG. 7

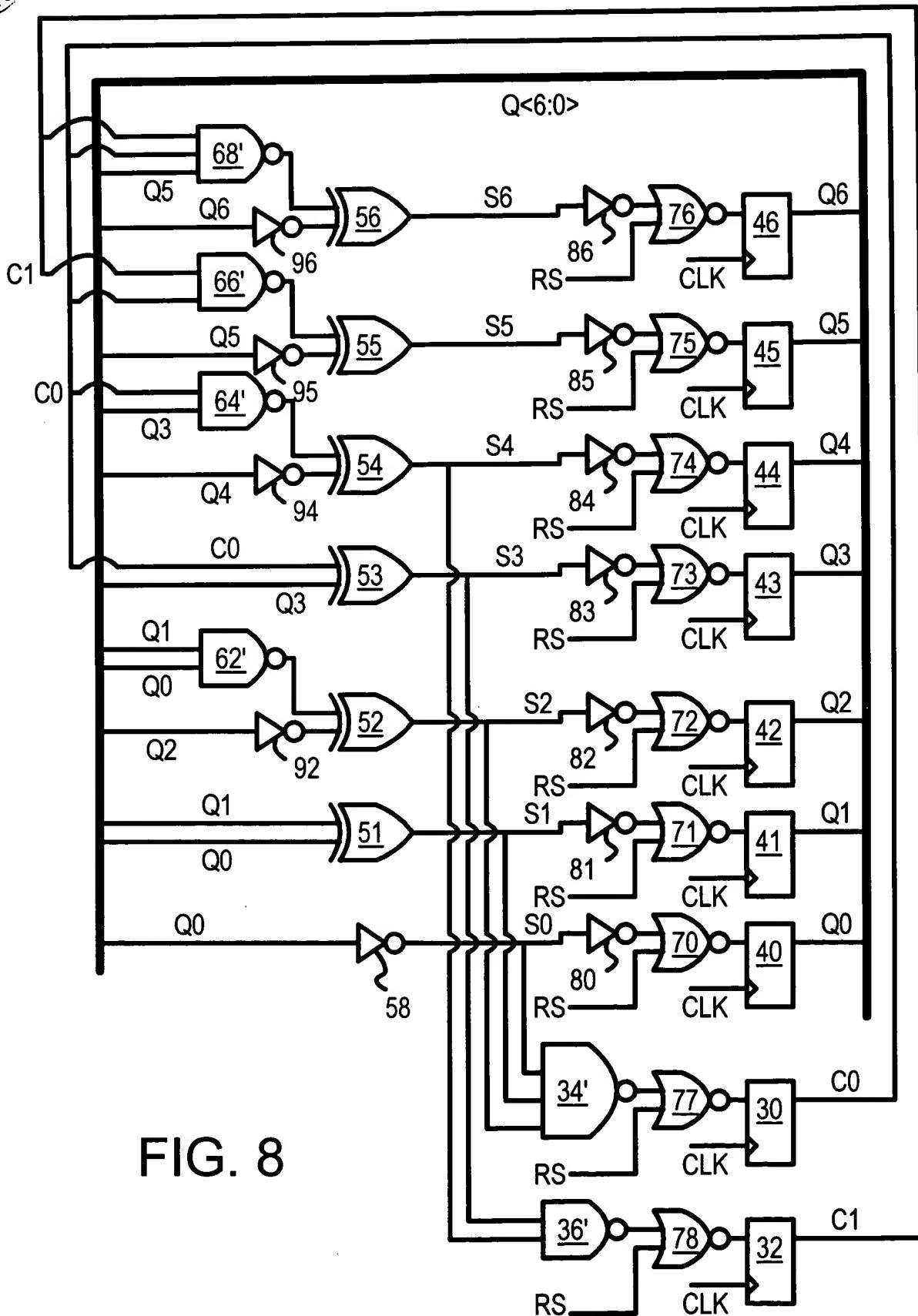


FIG. 8